



SPEC® CINT2006 Result

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Hewlett-Packard Company

ProLiant DL360 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECint®2006 = 16.2

SPECint_base2006 = 15.5

CPU2006 license: 3

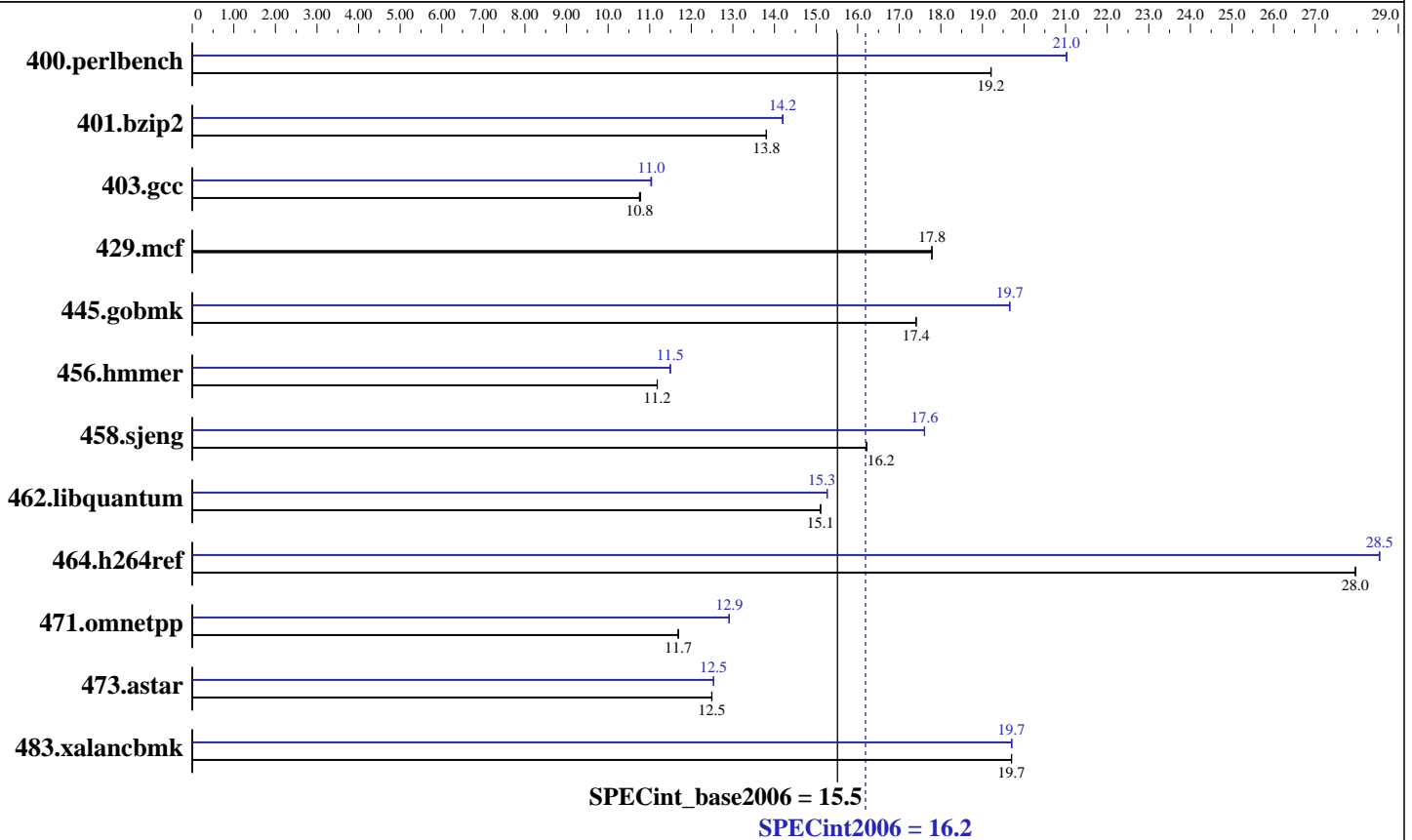
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2007

Hardware Availability: Jan-2007

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon X5355
 CPU Characteristics: 2.66 GHz, 2x4 MB L2 shared, 1333 MHz system bus
 CPU MHz: 2666
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8x2 GB PC2-5300 CL5)
 Disk Subsystem: 2x72 GB 10k SAS
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
 Compiler: Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z
 Package ID: W_CC_P_9.1.020
 Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)
 Auto Parallel: No
 File System: NTFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: MicroQuill SmartHeap Library 8.0



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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<u>509</u>	<u>19.2</u>	509	19.2	509	19.2	465	21.0	<u>465</u>	<u>21.0</u>	465	21.0
401.bzip2	<u>699</u>	<u>13.8</u>	699	13.8	699	13.8	<u>680</u>	<u>14.2</u>	680	14.2	680	14.2
403.gcc	749	10.8	747	10.8	<u>748</u>	<u>10.8</u>	730	11.0	<u>729</u>	<u>11.0</u>	729	11.0
429.mcf	<u>513</u>	<u>17.8</u>	513	17.8	513	17.8	<u>513</u>	<u>17.8</u>	513	17.8	513	17.8
445.gobmk	603	17.4	603	17.4	<u>603</u>	<u>17.4</u>	534	19.7	534	19.7	<u>534</u>	<u>19.7</u>
456.hammer	<u>834</u>	<u>11.2</u>	834	11.2	834	11.2	<u>812</u>	<u>11.5</u>	811	11.5	812	11.5
458.sjeng	746	16.2	<u>746</u>	<u>16.2</u>	746	16.2	<u>687</u>	<u>17.6</u>	687	17.6	687	17.6
462.libquantum	1372	15.1	1372	15.1	<u>1372</u>	<u>15.1</u>	1357	15.3	1357	15.3	<u>1357</u>	<u>15.3</u>
464.h264ref	<u>791</u>	<u>28.0</u>	791	28.0	791	28.0	775	28.6	<u>775</u>	<u>28.5</u>	775	28.5
471.omnetpp	535	11.7	<u>535</u>	<u>11.7</u>	534	11.7	<u>484</u>	<u>12.9</u>	484	12.9	484	12.9
473.astar	<u>562</u>	<u>12.5</u>	562	12.5	562	12.5	560	12.5	<u>560</u>	<u>12.5</u>	560	12.5
483.xalancbmk	<u>350</u>	<u>19.7</u>	350	19.7	350	19.7	350	19.7	<u>350</u>	<u>19.7</u>	350	19.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch disabled in BIOS.

Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
473.astar: -DSPEC_CPU_LITTLE_ENDIAN

Base Optimization Flags

C benchmarks:
-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

Continued on next page



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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
-link /FORCE:MULTIPLE
```

Peak Optimization Flags

C benchmarks:

```
400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000
shlw32m.lib -link /FORCE:MULTIPLE
```

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: basepeak = yes

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:

```
-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F512000000 shlw32m.lib -link /FORCE:MULTIPLE
```

Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>



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